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APPLICATION NO.	FILING DATE	1	IRST NAMED INVENTOR		ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/734,506			Seong-Hoon Lee		MIC-42	1009
APPLN. TYPE	SMALL ENTITY	ISSUE FI	er Pr	JBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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nonprovisional NO		\$1400		3300	31700	12222005
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CFR 1.363).	lenge address (or Change of	Correspondence	(1) the names of up to 3 registered patent attorneys or agents OR, alternatively,			
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Michael J. Chasan

Registration No.



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

: Seong-Hoon Lee Applicant

Application No.: 10/734,506 Confirmation No.: 1009

: December 12, 2003 Filed

: GENERATING MULTI-PHASE CLOCK SIGNALS For

USING HIERARCHICAL DELAYS

Group Art Unit : 2816

: An T. Luu Examiner

New York, New York 10020

December 20, 2005

Mail Stop Issue Fee Commissioner for Patents P. O. Box 1450

Alexandria, Virginia 22313-1450

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Date of Deposit: December 20, 2005

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